

電磁兼容原理與設計

話題：我擁有的EMC，EMI，可靠度方面的資料我擁有的EMC，EMI，可靠度方面的資料我將陸續上傳。

聯繫模式：QQ：16322127（可透過QQ索取，要標注EMC，EMI，可靠度字樣）

電磁兼容原理與設計（基礎知識）

Digital Signal Integrity-Modeling and Simulation with Interconnects and Package

EMC basic

EMC的民用

EMC技術介紹

EMC技術介紹

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ESD問答

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High-Speed Digital System Design

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特性阻抗

物料之研究

印刷電路板排版設計

Abatement of Static Electricity

An EMC Directive for the Next Century

Australian EMC Regulation and Routes to Compliance

Building a Bridge Between Product Safety and EMC

Commercial Practices Standard Set to Replace MIL-STD 1686

Developments in Electrical Safety Testing

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//Reliability Review of North American Gas/Electric System
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//Semiconductor Device Reliability Failure Models
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//Avoiding Vibration in Odd-Shaped
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//Better Accelerated Tests

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//Designing Electronics for High Vibration and Shock
//SUMMARY OF HALT AND HASS RESULTS AT AN ACCELERATED RELIABILITY TEST CENTER
//Highly Accelerated Stressing of Products With Very Low Failure Rates
//HASS DEVELOPMENT METHODOLOGY HOW TO DEVELOP A SCREEN,WHEN TO CHANGE A SCREEN, AND WHEN TO RE-PROVE A SCREEN
//honeywell reliability
//How to Justify Machinery Improvements
//IBM consulting-Becoming A Process Based Organization
//IBM演示技巧教程
//MTBF Report_13 October 04
//MTBF_Paper
//ACCELERATED RELIABILITY TEST TECHNIQUES USED TO FIND DEFECTS WITHIN PRINTED CIRCUIT BOARDS
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//An Extended Reliability Growth Model For Managing And Assessing Corrective Actions
//Reliability Monitor Report
//reliability tools and integration in the manufacturing phase
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//蒙特卡羅法在零件可靠性設計中的應用
//Failure Mode and Effects Analysis (FMEA) A Guide for Continuous Improvement for the Semiconductor Equipment Industry
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//TOOLS OF RELIABILITY ANALYSIS -- Introduction and FMEAs
//nasa fmea bibliography
//Philips FMEA English
//An Introduction to Risk/Hazard Analysis for Medical Devices
//Using Failure Mode Effect Analysis (FMEA) to Improve Service Quality Service Operations Management
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 //BioLine V3.0_C_2003植物生長試驗箱
 //Company Presentation V3.0_C_2003德國富奇介紹
 //Human Performance Testing V3.0_C_2003人體資源測試
 //IEC60068-3-5溫度變化率
 //IP-Overview_C工業防護
 //Pharma V3.0_C_2003製藥工業的藥品穩定性試驗
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//Guide to Use Aluminum Electrolytic Capacitors
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//Accelerated Thermal Cycling and Failure Mechanisms
//Accelerated_Standards_Transition_Plan_Approved_by_Board_06_04
//Accelerated stress testing
//the enviromental stress screen handbook
//Pre halt analysis
//談加速壽命試驗
//2003 CARTS Derating differences Ta-KO-AO
//sony ss-00259 handbook
//ADI Reliability Handbook
//stress test qualification for discrete semiconductors
//An Overview of Weibull Analysis
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//An Improved SPICE Capacitor Model
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//IEC 60950
//Improving the Performance of Your Root Cause Analysis (RCA) Program
//The Application of Accelerated Testing Methods and Theory Accelerated Testing Methods

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//Life Testing and Reliability Predictions for Electromechanical Relays

//MAXIM reliability report

//TESTING FOR RELIABILITY IN SPACE SYSTEMS

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// USB Series Connector

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//THE RETAIL ELECTRIC COMPETITION TASK FORCE

//Reliability as a Systems Engineering Investment Not Just a Cost

//Reliability Design Technology for Power Semiconductor Modules

//Reliability Programs

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//Reliability Testing and Data Analysis of High-Density Packages' Lead-Free Solder Joints

//Dynamic reliability and risk assessment of the accident localization system of the Ignalina
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//Semiconductor Device Reliability Failure Models

//Software Reliability Handbook Chapter 11

//SONY SS-00259-1第四版(英日)

//SONY00254-5-R4

//Standard Linear IC reliability

//Stress test Qualification for Integrated Circuits

//System Availability Modelling

//The Challenge of Supporting Aging Naval Weapon Systems Aging Naval Weapon Systems

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//ACPI Component Architecture Programmer Reference
//A critique of the EMC Directive

//Abatement of Static Electricity
//An EMC Directive for the Next Century
//COMPLIANCE SYSTEMS CORPORATION app_note_EN61000-4-5
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//Advanced Battery Engineering Facility
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//Audit Report AA 00-341 High Level Architecture
//Audit Report AA 01-128 Integrated System Control
//Audit Report AA 01-23 Simulation High Level Architecture
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//FMEA2
//FMEA3
//FMEA失效模式和效果分析
//how to selling_root_cause to management
//Philips FMEA
//Potential Failure Mode and Effects Analysis
//Random-Failure-Models
//ROOT CAUSE ANALYSIS
//root causea nalysis chapter1
//SURFACE VEHICLE RECOMMENDED PRACTICE
//WHAT MAKES A ROOT CAUSE FAILURE ANALYSIS PROGRAM SUCCESSFUL
//故障模式影響分析
//如何 行失效模式 影 分析

ESD

//Digital Phosphor Oscilloscopes
//A Safety Standard for Electrosensitive Protective Equipment
//Adding Value through Accredited Testing
//Littelfuse Cable Protectors for High Current Applications
//CMOS積體電路的ESD設計技術
//computer ESD solution
//Fundamentals of Electrostatic Discharge An Introduction to ESD
//ESD Suppression Technologies
//ESD Suppression Technologies ec622a ec622a

//Selecting an ESD Suppressor
//ESD Protection Audio Input and Output Lines
//Capacitance and Signal Integrity
//ESD Protection Digital Visual Interface Data Lines
//ESD Protection IEEE 1394 Data Lines
//ESD Protection USB 1.1 Data Lines
//ESD Protection USB 2.0 Data Lines
//ESD Protection Video Input and Output Lines
//General Purpose ESD Protection
//ESD Journal - The ESD & Electrostatics Magazine
//ESD protect
//ESD Standards
//Evaluation of Materials for Cleanliness and ESD Protective Properties
//Electrostatic Discharge (ESD) in Magnetic Recording Past, Present and Future
//Explosions and ESD
//From Electrostatics to ESD
//Fuse fact
//Ground planes for low cost boards
//Grounding Strategies for Printed Circuit Boards
//How Is Static Electricity Generated
//Is Static Electricity Static
//Littelfuse Resistors for Voltage Suppression
//SiVa ESD Demo
//The Competitive Advantage of Standards
//The Evolution of Guide into ISO 17025
//What It Means to ESD

HALT

//ENVIRONMENTAL EFFECTS
//筆記本電腦失效模式分析表
//測試前筆記本性能測試
//測試前后的機構電性功能驗證
//常見失效模式一覽表
//可靠性驗證測試
//失效分析是指研究產品潛在的或顯在的失效機理
//失效效應危害度一覽表
//ENVIRONMENTAL ENGINEERING CONSIDERATIONS AND LABORATORY TESTS
//A fundamental overview of accelerated-testing analytic models
//A5 P-FMEA
//accelerated and classical reliability methods integrated
//accelerated model

//accelerated test reference1
//accelerated test reference2
//accelerated test reference3
//accelerated test reference4
//美國可靠性強化試驗技術發展點評
//An approach to designing accelerated life-testing experiments
//Ast
//BCC-4V Halt Test
//Critical Analysis Team Report on Accelerated Waste Retrieval Final Design and Fixed Price Contracting
//Don't Let the Cost of HALT Stop You
//電子設備的可靠性設計技術
//FEMMA Technology Overview FEMMA Technology Overview
//fixturing China presentation 2-04
//FMEA5
//HALT & HASS1
//HALT GUIDELINE 2004
//HALT Guideline
//HALT HASS SEMINAR PRESENTED BY ENVIROTRONICS
//The Application of HALT for Increased Product Reliability
//加速試驗綜述
//HALT&HASS基礎篇 - 中文 - 2003
//HALT-HASS
//HALT-Testing With a Different Purpose
//Hass and Halt
//HASS of Products With Very Low Failure Rates
//high reliability challenge of broadband equipment
//Highly Accelerated Life Testing
//緊湊型節能燈壽命的常規試驗方法
//Material failure mechanisms and damage models
//MTBF Assurance test
//PCB relia design
//Quick guide Accelerated Life Testing Data Analysis Basics
//quick guide life data analysis
//可靠性設計
//Reliability Glossary
//reliability prediction VS HALT testing
//Searching for appropriate humidity accelerated migration reliability tests methods
//System reliability modeling considering the dependence of component environmental influences
//understanding accelerated life testing analysis

//what is HAST testing
//why HALT cannot produce a meaningful MTBF number and why this should not be concern
//高加速壽命試驗(HALT)與高加速應力篩選(HASS)
//失效率
//用高壓鍋做測試

//統計知識
//機率與統計入門
研究。
//Pipe Wall Thickness Decisions Using Weibull Analysis
//Weibull analysis for production data
//Weibull Analysis of Production Data
//方差分析(1)
//方差分析(2)
//機率統計
//高級基本統計
//質量管理中的技術統計

pcb
//BGA技術與質量控制
//Creep of Pb of Pb-free Solders free Solders
//intel glossary
//High Speed Board Design
//LEAD-FREE MANDATE PLUMBS NEW DESIGN CHALLENGES
//印製電路板設計原則和抗干擾措施
//PCB化學鍍鎳金工藝介紹
//SMT technology
//電子工業的絲網印刷
//電子裝聯焊接技術現狀與課題
//混合信號電路板的設計準則
//局部焊接的應用
//無鉛焊料的開發與應用
//新手上路認識PCB
//印製電路板的可靠性設計措施
//印製電路工藝創新探討

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//ATMEL可靠性報告
//MICROCHIP的可靠性報告
//Software Fault Tolerance

//高加速壽命試驗（HALT）與高加速應力篩選（HASS）
//論加速試驗
//HDBK-HALT-HASS
//LeCroy數字示波器操作手冊
//MTBF
//thermal module design
//散熱設計準則
//Vedio & Sound Technology
//振動測試概論
//Vibration Technology
//振動技術簡介
//An Overview of Vehicle Pass-by Noise
//Time Domain Acoustical Holography and Its Applications
//Key Steps and Methods in the Development of Low Noise Engines
//Squeak and Rattle State of the Art and Beyond
//Changing the Effective Mass to Control Resonance Problems
//Torsional Resonance Analysis in Air Handling Units
//Understanding the Physics of Electrodynamic Shaker Performance
//An Overview of Vehicle Pass-by Noise
//Newtonian Physics
//Conservation Laws
//Electricity and Magnetism
//Building Vibration Can Contaminate Clean Factories, Cleanrooms And Clean Activitie
//cots vibration testing
//Do We Know What We Are Doing
//Drop Tests vs Shock Table Transportation Tests
//Dynamic design
//Experimental Modal Analysis
//LASER ALIGNMENT SPECIFICATION FOR NEW AND REBUILT MACHINERY,
EQUIPMENT AND COMPONENTS
//GENERAL MOTORS CORPORATION VIBRATION STANDARD
//Going To Witness A Vibration Test
//Going To Witness A Vibration Test
//Guidelines for Jury Evaluations of Automotive Sounds
//King Design Test Specification 1.2
//Mechanical Waves
//Newtonian Physics
//FedEx Package_Testing_Procedures
//PSD pattern 及 total Grms 值的 算
//Refraction of Sound Waves
//Rotational_Shock

//shock test system
//Squeak and Rattle State of the Art and Beyond
//The Dynamic Vibration Absorber
//The Politics of Accelerated stress testing
//Time Domain Acoustical Holography and Its Applications
//Torsional Resonance Analysis in Air Handling Units
//Understanding the Physics of Electrodynamic Shaker Performance
//vibration test detail
//Vibration
//Vibrations and Wave
//包裝設計基礎
//包裝試驗
//多軸振動環境試驗技術
//海軍製造篩選程式
//可靠性振動基礎
//中高量級衝擊試驗技術
//weibull的有關問題(共30份)
//BGA可靠性表征項目：溫度循環試驗
//ESD損傷實例
//半導體分立器件的可靠性設計
//半導體積體電路的可靠性設計
//半導體器件失效原因分析
//當代質量觀與可靠性
//電氣繼電器(第20部分保護系統)
//電容器的可靠性設計
//電子元器件的防靜電應用
//電子元器件的防浪涌應用
//電子元器件的防噪聲應用
//電子元器件的抗輻射應用
//電子元器件的可靠性安裝
//電子元器件的選擇和應用
//電子元器件的質量與可靠性軍用標準體系
//電子元器件電路佈局的可靠性設計
//電子元器件控制
//電阻器的使用問題
//電阻器與電位器的可靠性
//方案階段
//非氣密性鐳射模塊的高加速壽命試驗
//概述
//矽可控整流器（SCR）的使用問題
//化學物理電源的可靠性設計

//環境加速試驗的條件
//基本概念二
//基本概念三
//基本概念四
//基本概念五
//基本概念一
//計算機輔助可靠性評價技術
//可靠性工程技術現狀2001年報告
//可靠性工作主要內容
//可靠性技術講座(下)
//可靠性篩選
//微電路的型號命名和採買
//微電路的選用原則
//微電路的質量、可靠性等級
//微電路降額設計指南
//預防ESD損傷的措施
//元器件優選目錄編製要求
//35 MAINTENANCE AUDITS
//FAILURES AND ERRORS
//A Metric for Focusing Reliability Efforts
//AN OVERVIEW OF WEIBULL Chpt1
//ASTM_testing
//Availability and Reliability
//Ball-Bearing-Data-by-Lieblein-and-Zelan
//basic of oil analysis
//business centered maintenance
//Project Management Professional Certification Handbook 03-2002
//Changing_your_organization_for_better1
//Changing_your_organization_for_better2
//Changing_your_organization_for_better3
//Changing_your_organization_for_better4
//Changing_your_organization_for_better5
//Cost Of Unreliability
//Cost_Effective_Calibration
//定義必要的可靠性
//Distribution & Logistics Strategy
//Establishing a sense of urgency
//執行測試
//Heat Exchanger IRIS Wall Thickness And Gumbel Smallest Distribution
//How To Justify Equipment Improvements Using
//Integrated Service Technology

//Key_Performance_Indicators
 //manufacturing and business excellence
 //maximizing_maintenance_profits
 //micro reliability and lifetime estimation
 //Moving from a Repair focused to a Reliability focused Culture
 //Moving from a Repair-focused to a Reliability-focused Culture
 //MTBF for notebook
 //notebook test Plan
 //OCE
 //Predict Future Failures From Your Maintenance Records
 //REDUCING THE COST OF PREVENTIVE MAINTENANCE
 //reliability glossary
 //Reliability STD
 //Reliability and dependability standards are described
 //ROHS E文版
 //ROHS中文版
 //Comparison of Reliability-Availability Mission Simulators
 //Small Sample Size Datasets Help or Hindrance
 //Solder Data Practice Guide
 //SONY STANDARD SS-00259 中英文版
 //VIII. STANDARDS AND QUALITY CERTIFICATION FOR QUALITY
 SYSTEMS,SAFETY, ANDRELIABILITY OF SEMICONDUCTOR DEVICES
 //statistical investigation of fatigue life of deep-groove ball bearings
 //UK Defence Standardization
 //WEEE中文版
 // 子 品製造防 范
 //美國可靠性工程試題集
 //威布爾分佈壽命分析
 //晶片驗證測試及失效分析

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MIL-HDBK-17 1F,2F,3F,4F,5F
 MIL-STD-883 E,Notice1,2,3,4,5
 MIL-STD-461 E , CROSSREFERENCE
 MIL-STD-750D Notice1,2,3,4,5
 MIL-STD-1246 C , 1 , 2 , 3 , 4 , 5
 MIL-HDBK-5 (H , NOTICE)
 MIL-STD-810

Number Revision Date Title

MIL-B-5085 B 10/64 Bonding for Aerospace Systems

MIL-HDBK-5H - 12/98 Metallic Materials and Elements(The original issue is 20 MB; Notice 1 is 42MB
Notice 1 10/02
MIL-HDBK-17 Composite Materials Handbook
 Volume 1 F 06/02 Polymer Matrix/Guidelines for Characterization
 Volume 2 F 06/02 Polymer Matrix/Materials Properties
 Volume 3 F 06/02 Polymer Matrix/Materials Usage, Design and Analysis
 Volume 4 F 06/02 Metal Matrix Composites
 Volume 5 - 06/02 Ceramic Matrix Composites
MIL-HDBK-340 Notice 1 10/94 Application Guidelines for MIL-STD-1540
MIL-HDBK-343 - 02/86 Design, Construction, and Test Requirements for One-of-a-Kind
Spacecraft
MIL-M-38510 J 11/91 Military Specification, Microcircuits
MIL-PRF-13830 B 01/97 Optical Component Inspection
MIL-PRF-19500 M 10/99 Performance Specification, Semiconductor Devices
QML-19500 22 05/04 Qualified Manufacturers List
MIL-PRF-31032 - 11/95 Printed Circuit Board, General Spec.
MIL-PRF-38534 E 01/03 Performance Specification, Hybrid Circuits
QML-38534 48 03/04 Qualified Manufacturers List
MIL-PRF-38535 F 12/02 Performance Specification, Integrated Circuits
QML-38535 17 07/03 Qualified Manufacturers List
MIL-STD-202 G 02/02 Test Method Standard, Electronic Parts
MIL-STD-461 E 08/99 Control of Electromagnetic Interference
 Draft = 06/99 461 Draft in MSWord Format
 Cross Reference = 03/01 Comparison of 461E with other standards
MIL-STD-750 D 02/95 Test Method, Semiconductor Devices
Notice 1 05/95
Notice 2 02/96
Notice 3 02/00
Notice 4 04/01
Notice 5 11/02
MIL-STD-810 F 08/02 Test Method Standard/Env. Eng. Tests
MIL-STD-883 E 12/96 Test Method Standard/Microcircuits
Notice 1 12/97
Notice 2 08/98
Notice 3 11/99
Notice 4 12/00
Notice 5 03/03
MIL-STD-1246 C 04/94 Cleanliness Levels
Notice 1 05/94
Notice 2 12/94

Notice 3 06/98

Notice 4 02/02

MIL-STD-1540 E-Draft 12/02 Test Requirements for Launch & Space Vehicles(restricted access)

MIL-STD-1553 B 09/76 Multiplex Data Bus

Notice 1 02/80

Notice 2 09/86

Notice 3 01/93

Notice 4 01/96

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REPORT # TITLE DATE NOTES

MIL-HDBK-1512 ELECTROEXPLOSIVE SUBSYSTEMS, ELECTRICALLY INITIATED, DESIGN REQUIREMENTS AND TEST METHODS 9/30/1997

MIL-HDBK-1857 GROUNDING, BONDING AND SHIELDING DESIGN PRACTICES 3/27/1998

MIL-HDBK-235-1B ELECTROMAGNETIC (RADIATED) ENVIRONMENT CONSIDERATIONS FOR DESIGN AND PROCUREMENT OF ELECTRICAL AND ELECTRONIC EQUIPMENT, SUBSYSTEMS AND SYSTEMS 5/1/1993

MIL-HDBK-235-1B(1) ELECTROMAGNETIC (RADIATED) ENVIRONMENT CONSIDERATIONS FOR DESIGN AND PROCUREMENT OF ELECTRICAL AND ELECTRONIC EQUIPMENT, SUBSYSTEMS AND SYSTEMS 12/22/2000 Notice of Validation

MIL-HDBK-237C ELECTROMAGNETIC ENVIRONMENTAL EFFECTS AND SPECTRUM CERTIFICATION GUIDANCE FOR THE ACQUISITION PROCESS 7/17/2001

MIL-HDBK-240 HAZARDS OF ELECTROMAGNETIC RADIATION TO ORDNANCE (HERO) TEST GUIDE 11/1/2002

MIL-HDBK-263B ELECTROSTATIC DISCHARGE CONTROL HANDBOOK FOR PROTECTION OF ELECTRICAL AND ELECTRONIC PARTS, ASSEMBLIES, AND EQUIPMENT (EXCLUDING ELECTRICALLY INITIATED EXPLOSIVE DEVICES) (METRIC) 7/31/1994

MIL-HDBK-274 ELECTRICAL GROUNDING FOR AIRCRAFT SAFETY 11/1/1983

MIL-HDBK-274(1) ELECTRICAL GROUNDING FOR AIRCRAFT SAFETY 6/29/1990 Change Notice 1

MIL-HDBK-293 ELECTRONIC COUNTER-COUNTERMEASURES CONSIDERATIONS IN RADAR SYSTEMS ACQUISITION 6/5/1987

MIL-HDBK-294 ELECTRONIC COUNTER-COUNTERMEASURES CONSIDERATIONS IN NAVAL COMMUNICATION SYSTEMS 12/31/1986

MIL-HDBK-335 MANAGEMENT AND DESIGN GUIDANCE ELECTROMAGNETIC RADIATION HARDNESS FOR AIR LAUNCHED ORDNANCE SYSTEMS 1/15/1981

MIL-HDBK-335(2) MANAGEMENT AND DESIGN GUIDANCE ELECTROMAGNETIC
RADIATION HARDNESS FOR AIR LAUNCHED ORDNANCE SYSTEMS 12/28/1992
Notice of Validation
MIL-HDBK-419A GROUNDING, BONDING, AND SHIELDING FOR ELECTRONIC
EQUIPMENTS AND FACILITIES 12/29/1987
MIL-HDBK-454A GENERAL GUIDELINES FOR ELECTRONIC EQUIPMENT 11/3/2000

REPORT # TITLE DATE

DI-EMCS-80199B Electromagnetic Interference Control Procedures (EMICP) 8/20/1990
DI-EMCS-80200B Electromagnetic Interference Test Report (EMITR) 8/20/1990
DI-EMCS-80201B Electromagnetic Interference Test Procedures (EMITP) 8/20/1990
DI-EMCS-81295 Electromagnetic Verification Procedures (EM EVP) 11/25/1992
DI-EMCS-81528 Electromagnetic Compatibility Program Procedures 11/14/1996
DI-EMCS-81540 Electromagnetic Environmental Effects (E3) Integration and Analysis Report
(E3IAR) 12/19/2002
DI-EMCS-81541A Electromagnetic Environmental Effects (E3) Verification Procedures
(E3VP) 12/19/2002
DI-EMCS-81542A Electromagnetic Environmental Effects (E3) Verification Report (E3VR)
12/19/2002
DI-MISC-81113 Radar Spectrum Management (RSM) Test Plan 12/8/1990
DI-MISC-81114 Radar Spectrum Management (RSM) Control Plan 12/5/1990
DI-RELI-80669A Electrostatic Discharge (ESD) Control Program 8/28/1992
DI-RELI-80670A Reporting Results of Electrostatic Discharge (ESD) Sensitivity Tests of
Electrical and Electronic Parts Assemblies and Equipment 8/28/1992
DI-RELI-80671A Handling Procedures for Electrostatic Discharge (ESD) Sensitive 8/28/1992